

**Amendments to the Abstract:**

**Please replace the previous Abstract page with the following redlined Abstract:**

~~“VOLTAGE BOOSTER DEVICE AND MEMORY SYSTEM”~~

**ABSTRACT**

Voltage booster device (3) such as to selectively assume an active status and a stand-by status, said device comprising: \_a first terminal (15) such as to assume a respective electric potential and associated to a first capacitor (16), \_a second terminal (10) associated to a second capacitor (11) and selectively connectable to the first terminal (15), \_~~characterised~~characterized in that it also comprises a discharge circuit ~~electrical means (100)~~ for discharging the first capacitor thus reducing in module the electrical potential of the first terminal (15), the ~~electrical means~~ discharge circuit being activated to functioning when said device in the stand-by status and the second terminal (10) is disconnected from said first terminal (15).

Figure 1